## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4032	bank adj address	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/10/08 08:04
L2 _	3322	365/222.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/10/08 08:04
L3	232	1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO	OR .	ON .	2006/10/08 08:05
L4	16	you-min-young.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR .	ON	2006/10/08 08:05

10/8/06 8:05:20 AM C:\Documents and Settings\TPhan2\My Documents\EAST\Workspaces\default.wsp Page 1



Day: Sunday Date: 10/8/2006

Time: 08:05:54

## **Inventor Name Search Result**

Your Search was:

Last Name = YOU

First Name = MIN-YOUNG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09198521	5986956	150	11/24/1998	REPAIR CONTROL CIRCUIT	YOU, MIN-YOUNG
09304643	6111779	150	05/04/1999	CELL STRUCTURE FOR LOW ELECTRIC POWER STATIC RAM	YOU, MIN-YOUNG
09377163	6128218	150	08/19/1999	HYBRID MEMORY DEVICE	YOU, MIN-YOUNG
09461413	6359473	150	12/16/1999	AMPLIFIER FOR USE IN SEMICONDUCTOR INTEGRATED CIRCUITS	YOU, MIN-YOUNG
09562943	6208581	150	05/02/2000	Hybrid memory device and method for controlling same	YOU, MIN-YOUNG
10061372	6703871	150	02/04/2002	AMPLIFIER FOR USE IN SEMICONDUCTOR INTEGRATED CIRCUITS	YOU, MIN-YOUNG
10749350	Not Issued	77	12/31/2003	Semiconductor memory device for reducing peak current during refresh operation	YOU, MIN-YOUNG

Inventor Search Completed: No Records to Display.

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